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Application Number	10/802,049
Filing Date	March 17, 2004
First Named Inventor	Frampton ELLIS, III
Art Unit	2136 2436
Examiner Name	LOUIE
Attorney Docket Number	313449-P0004 C1

Sheet	1	of	2	Attorney Docket Number	313449-P0004 C1
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Examiner Initials*	Cite No.	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	1*
		Country Code* Number* Kind Code* (if known)				
Examiner Signature	/Oscar Louie/ (06/11/2009)			Date Considered	06/11/2009	

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**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

(Use as many sheets as necessary)

Sheet	2	of	2	Attorney Docket Number	313449-P0004 C1
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Application Number	10/802,049
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First Named Inventor	Frampton ELLIS, III
Art Unit	2138
Examiner Name	LOUIE

NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
		Wilkinson, Barry et al., "Parallel Programming: Techniques and Applications Using Networked Workstations and Parallel Computers", Chapter 4, Prentice-Hall, Inc., 1999.	
		Baker, Lou et. al., "Parallel Programming", Chapter 4, McGraw Hill Companies, Inc., 1996.	
		Kayssi, A.; Harik, L.; Ferzli, R.; Fawaz, M; "FPGA-based Internet Protocol Firewall Chip"; Electronics, Circuits and Systems, 2000. ICECS 2000. The 7th IEEE International Conference on Volume 1, 17-20 Dec. 2000 Page(s):316-319 vol. 1.[retrieved from IEEE database 6.9.08]	
		Office Action issued in Application No. 10/663,911 mailed September 26, 2008	
		Notice of Allowance issued in Application No. 09/884,041 mailed October 9, 2008	
Examiner Signature	/Oscar Louie/ (06/11/2009)	Date Considered	06/11/2009

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